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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/750,989	12/31/2003	Ryuji Maeda	CX03021USU (03CXT0033D)	5490
34408	7590	05/27/2005	EXAMINER	
THE ECLIPSE GROUP 10453 RAINTREE LANE NORTHRIDGE, CA 91326			NGUYEN, PATRICIA T	
			ART UNIT	PAPER NUMBER
			2817	

DATE MAILED: 05/27/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

10/750,989

Applicant(s)

MAEDA, RYUJI

Examiner

Patricia T. Nguyen

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-36 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-4, 6-18 and 20-36 is/are rejected.
- 7) ☒ Claim(s) 5 and 19 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_.

## DETAILED ACTION

### ***Claim Rejections - 35 USC § 102***

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-3, 6, 7, 15, 16, 20, 21, 31, 32, 34, and 35 are rejected under 35

U.S.C. 102(b) as being anticipated by Furlow, U.S. Patent # 5,072,199.

Fig. 2 of Furlow discloses a circuit comprising: transistors Q3, Q5 can be read as a plurality of active circuits or means for producing the plurality of output signals; input RF IN can be read as a received input signal; output signal OUT1 can be read as a corresponding split output signal (see spec. col. 6, lines 1-17); node RF IN, capacitor CB and node A can be read as means for receiving the input signal.

Regarding claim 15, transistor Q1 can be read as a first gain stage or means for amplifying the input signal prior to its being split into a plurality of output signals.

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1, 2, 10-16, and 24-36 are rejected under 35 U.S.C. 102(e) as being anticipated by Fanous et al., U.S. Patent # 6,586,996 B2.

Figs. 5B-8B, 14, and 17 of Fanous et al. discloses a circuit comprising: in Fig. 14, differential amplifiers 1408, 1410, 1412 can be read as a plurality of active circuits or means for producing the plurality of output signals; input signal to differential amplifier 1404 can be read as a received input signal; output signals at outputs of 1408, 1410,

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1412 can be read as a corresponding split output signal; transformer at input of differential amplifier 1404 can be read as means for receiving the input signal.

Regarding claim 15, differential amplifier 1404 can be read as a first gain stage or means for amplifying the input signal prior to its being split into a plurality of output signals.

Regarding claims 25 and 26, AGC circuit 1406 can be read as a controller.

Regarding claims 28 and 30, see Figs. 5B - 8B.

### ***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 4, 8, 9, 17, 18, 22, and 23 are rejected under 35 U.S.C. 103(a) as being unpatentable over Furlow, U.S. Patent # 5,072,199.

Although Furlow does not mention that his transistors are bipolar or CMOS or MOSFET, it would have been obvious at the time the invention was made to a person having ordinary skill in the art to substitute FET for different types of transistors such as bipolar or CMOS or MOSFET since such substitution is well known in the art in the absence of unexpected results and this is a matter of design choice.

***Allowable Subject Matter***

Claims 5 and 19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

***Conclusion***


The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. U.S. Patents # 6,577,198 B1, # 5,559,472, # 5,546,050, and # 3,978,422 contain some limitations of the claimed invention.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Patricia T. Nguyen whose telephone number is (703) 308-1927. The examiner can normally be reached on 6:30 AM - 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on 703-309-4940. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PTN  
May 24, 2005

  
**PATRICIA NGUYEN**  
**PRIMARY EXAMINER**